

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Currently amended) A method for controlling access to resources shared among a plurality of logical processors, comprising:

obtaining exclusive access for a first logical processor to a resource descriptor describing a usage allocation of said shared resources;

querying said resource descriptor to determine whether resources needed by said first logical processor are available;

if resources needed by said first logical processor are available, updating said resource descriptor to reserve said resources for exclusive use by said first logical processor; and

releasing said exclusive access for said first logical processor to said resource descriptor.

2. (Original) The method of claim 1, further comprising:

if said resources needed by said first logical processor are not available, releasing said exclusive access for said first logical processor to said resource descriptor.

3. (Original) The method of claim 1, further comprising, after the releasing, accessing a shared resource by said first logical processor.

4. (Original) The method of claim 1, further comprising:

after exclusive access for said first logical processor to said resource descriptor is released, obtaining exclusive access for a second logical processor to said resource descriptor;

querying said resource descriptor to determine whether resources needed by said second logical processor are available;

if resources needed by said second logical processor are available, updating said resource descriptor to reserve said resources for the exclusive use of said second logical processor; and

releasing said exclusive access for said second logical processor to said resource descriptor.

5. (Original) The method of claim 4 further comprising:

if said resources needed by said second logical processor are not available, releasing said exclusive access for said second logical processor to said resource descriptor.

6 - 9. (Canceled)

10. (Currently amended) A device An apparatus comprising:

a plurality of logical processors;  
a plurality of resources shared by said plurality of logical processors;  
a resource descriptor to identify a status of said shared resources; and  
a semaphore to reserve exclusive access for one of said plurality of logical processors to said resource descriptor.

11. (Currently amended) The device apparatus of claim 10, further comprising ~~program code executable by said plurality of logical processors to control access to said shared resources;~~

~~wherein said program code includes instructions for logic to:~~

~~causing- cause a first logical processor to update said semaphore to reserve exclusive access to said resource descriptor;~~

causing cause said first logical processor to update said resource descriptor to reserve exclusive use of at least a first resource of said shared resources; and

subsequently causing cause said first logical processor to update said semaphore to release said exclusive access.

12. (Currently amended) The device apparatus of claim 11, said program code further comprising instructions for logic to further:

causing cause a second logical processor to update said semaphore to reserve exclusive access to said resource descriptor;

causing cause said second logical processor to update said resource descriptor to reserve exclusive use of at least a second resource of said shared resources; and

subsequently causing cause said second logical processor to update said semaphore to release said exclusive access;

wherein after reserving exclusive use of said first and second resources, respectively, said first and second logical processors concurrently use said first and second resources, respectively.

13. (Currently amended) A computer usable medium tangibly embodying logical processor executable instructions for controlling access to shared resources in a device comprising a plurality of logical processors, a first logical processor requiring at least a first resource of said shared resources, and a second logical processor requiring at least a second resource of said shared resources, said instructions when executed causing said first logical processor A machine-readable medium storing instructions to implement a process perform a method comprising:

by a first logical processor,

setting a lock bit in a semaphore register to reserve exclusive access to a resource descriptor register;

generating a first bitmap identifying said a first required resource;

applying said first bitmap to said resource descriptor register to reserve said first required resource;

re-setting said semaphore lock bit to release said exclusive access; and using said first resource.

14. (Currently amended) The computer usable machine-readable medium of claim 13, said ~~instructions when executed further causing said second logical processor to implement a process comprising method further comprising:~~

by a second logical processor,

when after said first logical processor has re-set said semaphore lock bit, setting said semaphore lock bit;

generating a second bitmap identifying ~~said a~~ second required resource;

applying apply said second bitmap into said resource descriptor register to reserve said second required resource;

re-setting re-set said semaphore lock bit to release said exclusive access; and

using said second resource;

wherein said first and second logical processors use said first and second resources in parallel.

15. (Currently amended) The computer usable machine-readable medium of claim 13, wherein said setting a lock bit comprises supplying an identifier of said first logical processor for writing into said semaphore register.

16 - 17. (Canceled)

18. (New) A system comprising:

a plurality of logical processors;

a plurality of resources to be shared by said logical processors;

a resource descriptor to control access to said resources;

a semaphore register to reserve exclusive access for one of said plurality of logical processors to said resource descriptor; and  
access control logic to allocate one or more of said shared resources only when granted exclusive access to said resource descriptor by said semaphore register.

19. (New) The system of claim 18, wherein said resource descriptor includes a plurality of fields each to associate a resource with a logical processor identifier.

20. (New) The system of claim 1, wherein said access control logic is to  
obtain a lock on said semaphore register to reserve exclusive access to said resource descriptor,  
determine whether a needed resource is available based on said resource descriptor,  
if so, reserve the resource, and  
release the lock on the semaphore register.

21. (New) The system of claim 20, wherein said access control logic is further to reserve one or more resources by assigning a logical processor identifier to a corresponding resource.

22. (New) The system of claim 18, further comprising unlock logic to prevent a failing logical processor from retaining a lock on the semaphore register.

23. (New) The system of claim 22, wherein the unlock logic includes causing a logical processor different from the failing logical processor to call a semaphore lock release routine and pass the routine the identifier of the failing logical processor.